



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit : 2814

Examiner: Mr. Phat X. Cao

AF-12814  
(5)  
#11/B(AE)  
12/16/01  
Purley

In re PATENT APPLICATION of

Applicants : Yasutaka SAKAINO, et al.

Appln. No.: 09/229,628

Filed : January 13, 1999

For : SEMICONDUCTOR INTEGRATED  
CIRCUIT

Atty. Dkt. :  
32014-141666 (Formerly OKI 4646.01)

AMENDMENT AFTER  
FINAL REJECTION

November 29, 2001

Customer No.



26694

PATENT TRADEMARK OFFICE

RECEIVED  
FEB - 3 2001  
MAIL ROOM

Assistant Commissioner for Patents  
Washington, D.C. 20231

**ATTENTION: BOX AF**

Sir:

In response to the Office Action of October 22, 2001, the period for reply to which has been set to expire on January 22, 2002, please amend the above-identified application as follows:

**IN THE SPECIFICATION/ABSTRACT**

Please enter the attached Substitute Specification (including the abstract, but not the claims)

PLEASE  
ENTER AMENDMENT  
PC  
12/17/01